

FIG. 1

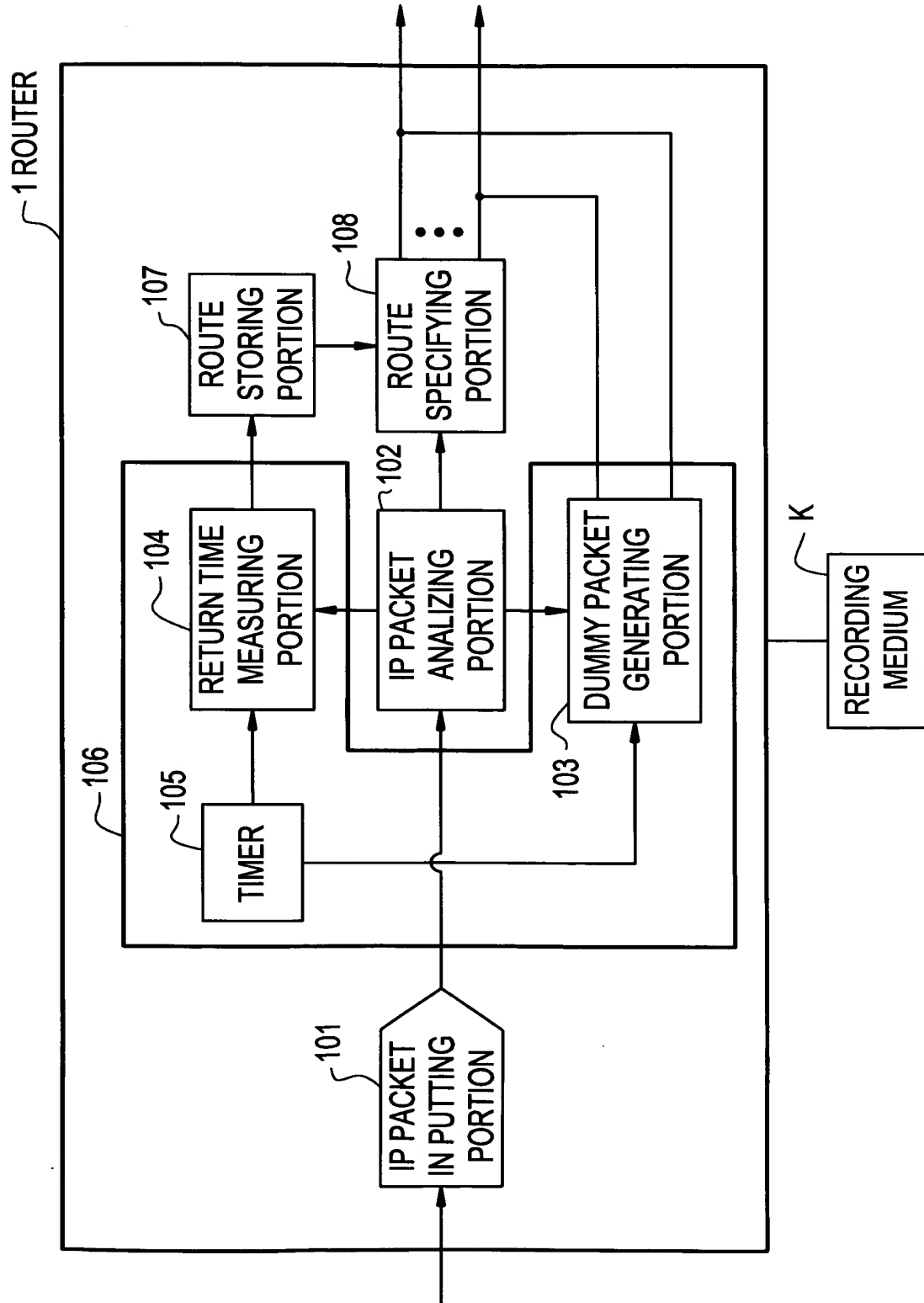


FIG. 2

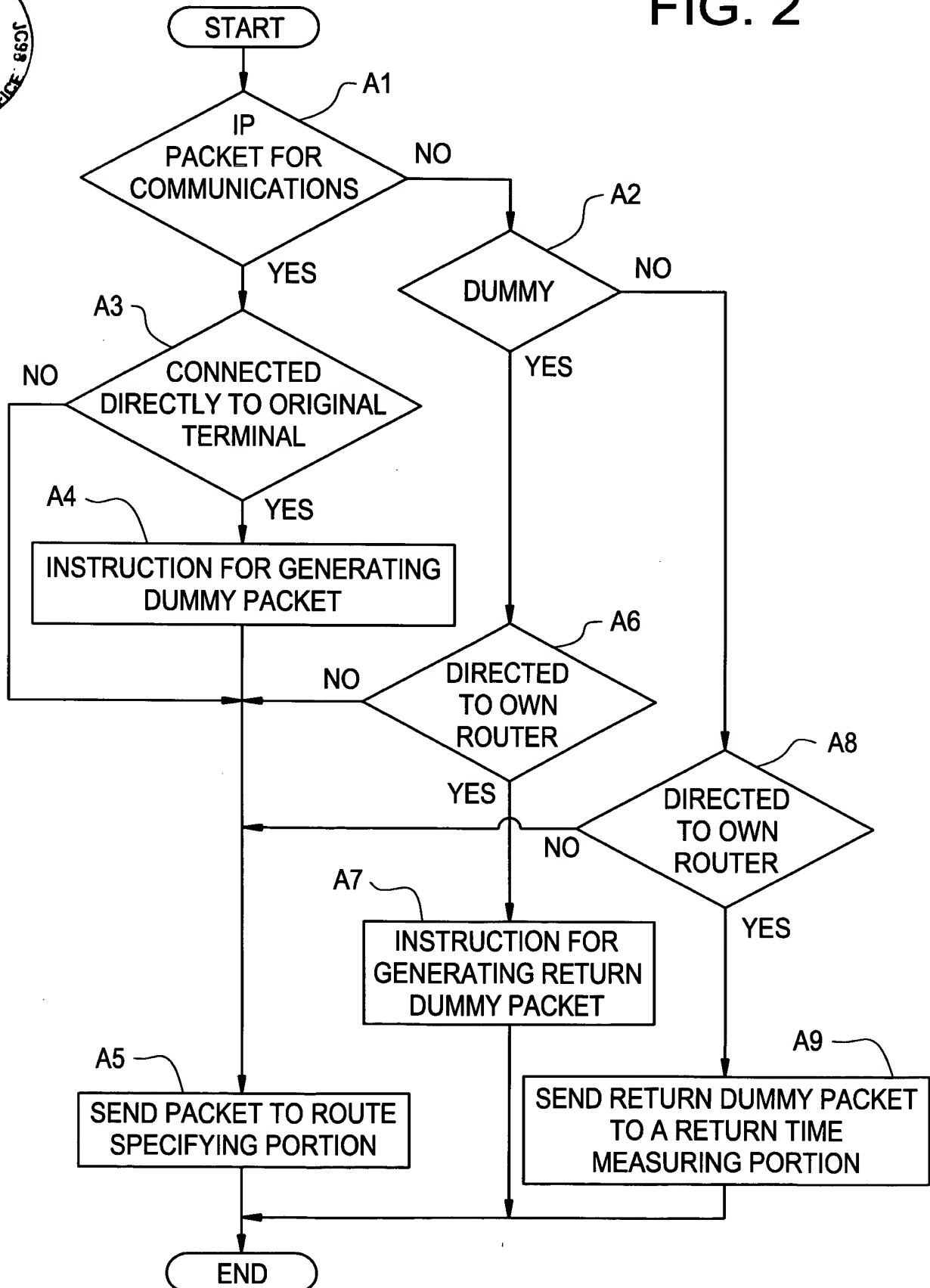


FIG. 3

0	1	2	3	4	5	6	7
PRECEDENCE			D	T	R	0	0

Bits 0-2 : Precedence, 0(Lower Precedence)~7(Higher Precedence).

Bit 3 : 0 = Normal Delay,

1 = Low Delay.

Bit 4 : 0 = Normal Throughput,

1 = High Throughput.

Bit 5 : 0 = Normal Reliability,

1 = High Reliability.

Bit 6, 7 : Reserved for Future Use.





FIG. 4

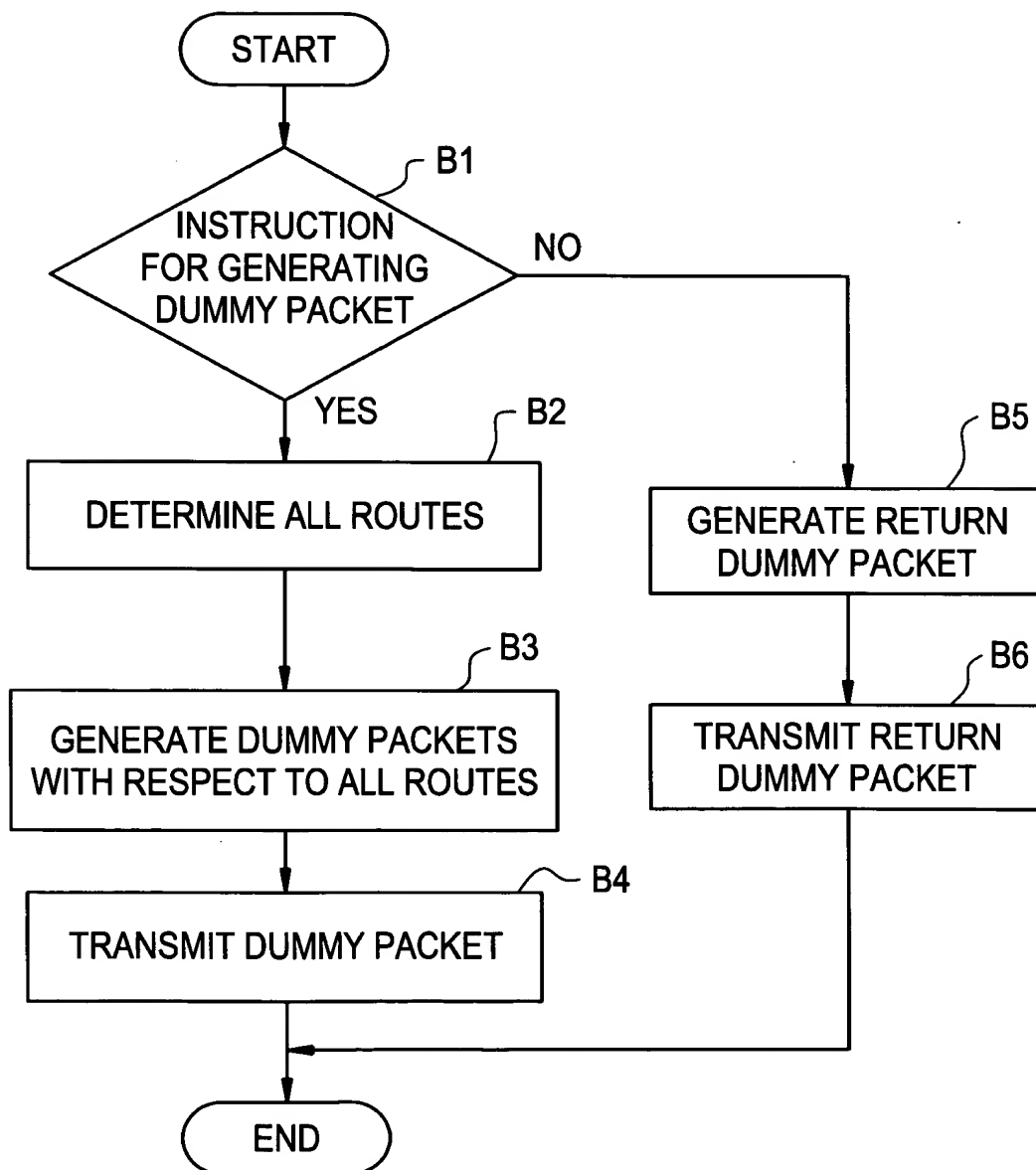
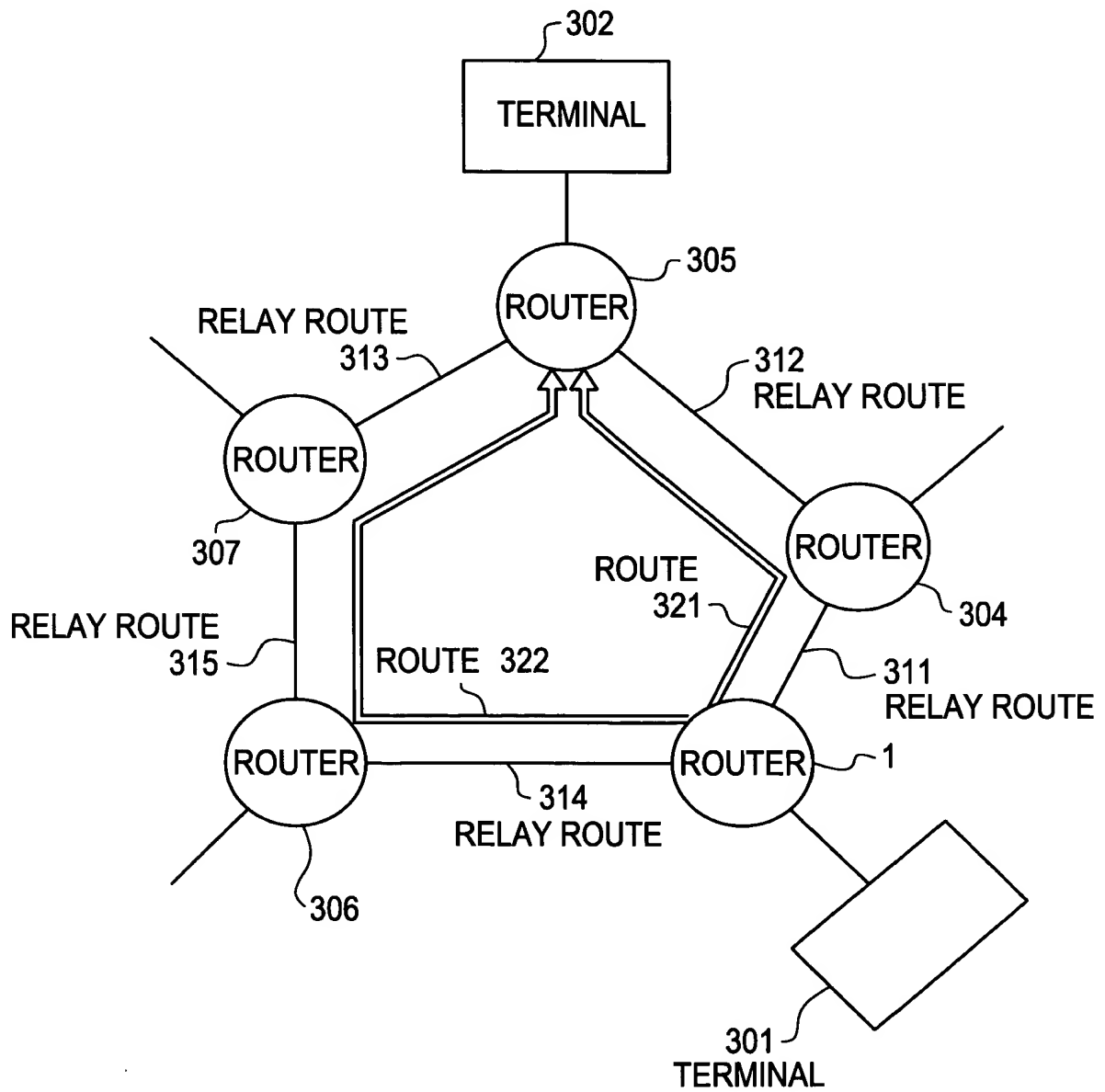




FIG. 5



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FIG. 6

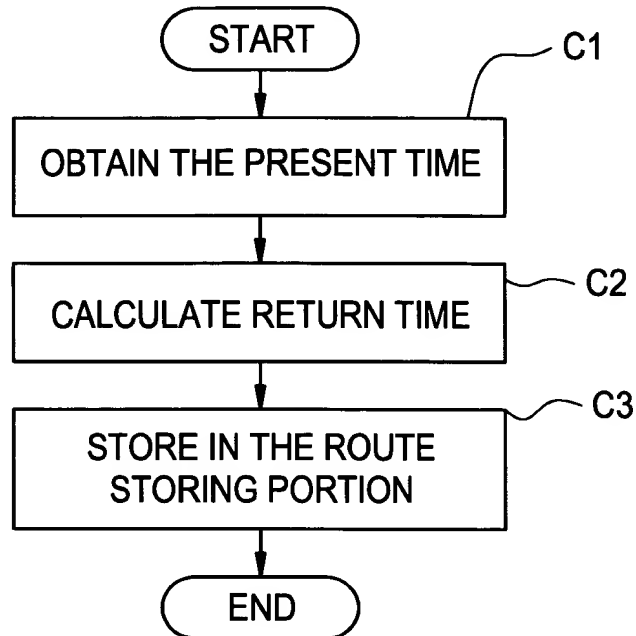


FIG. 7A

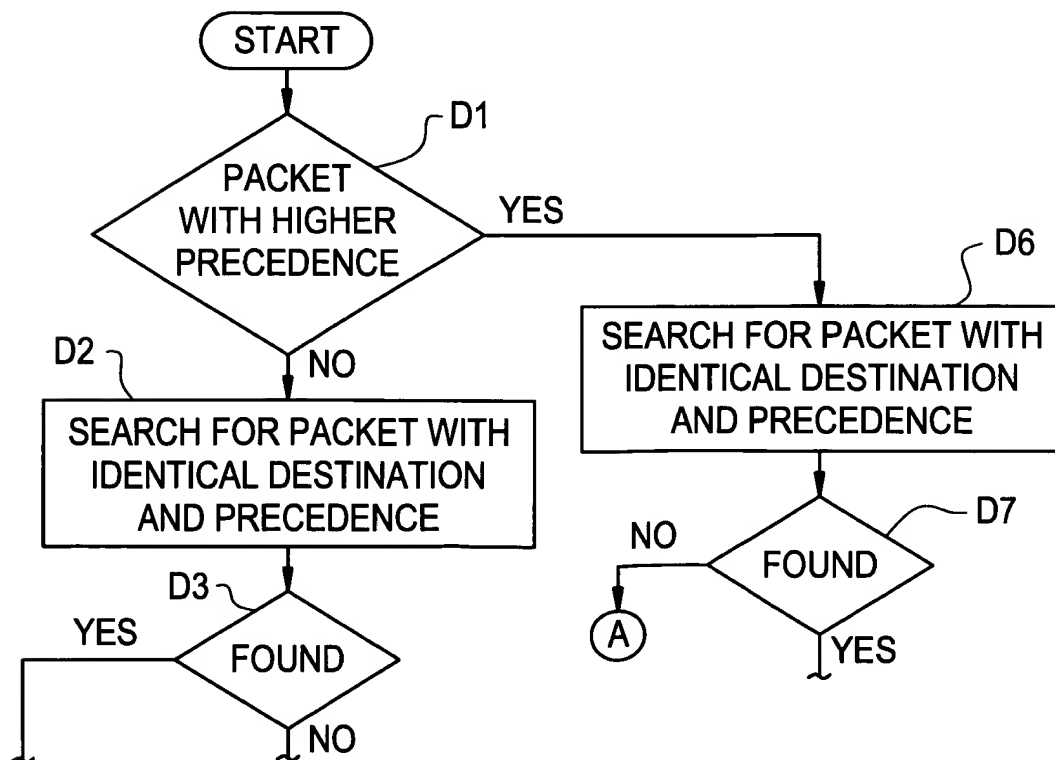


FIG. 7B

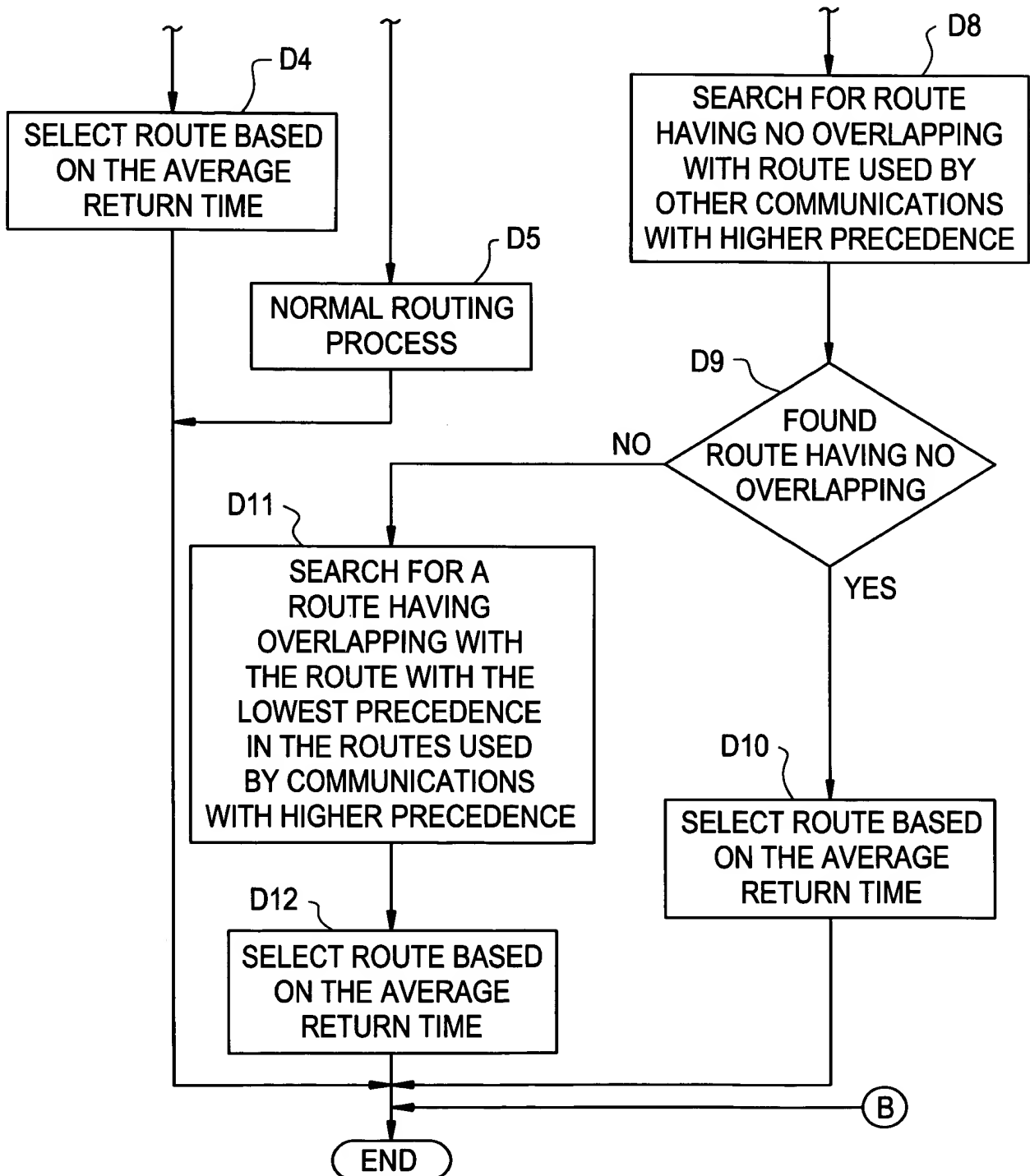


FIG. 8

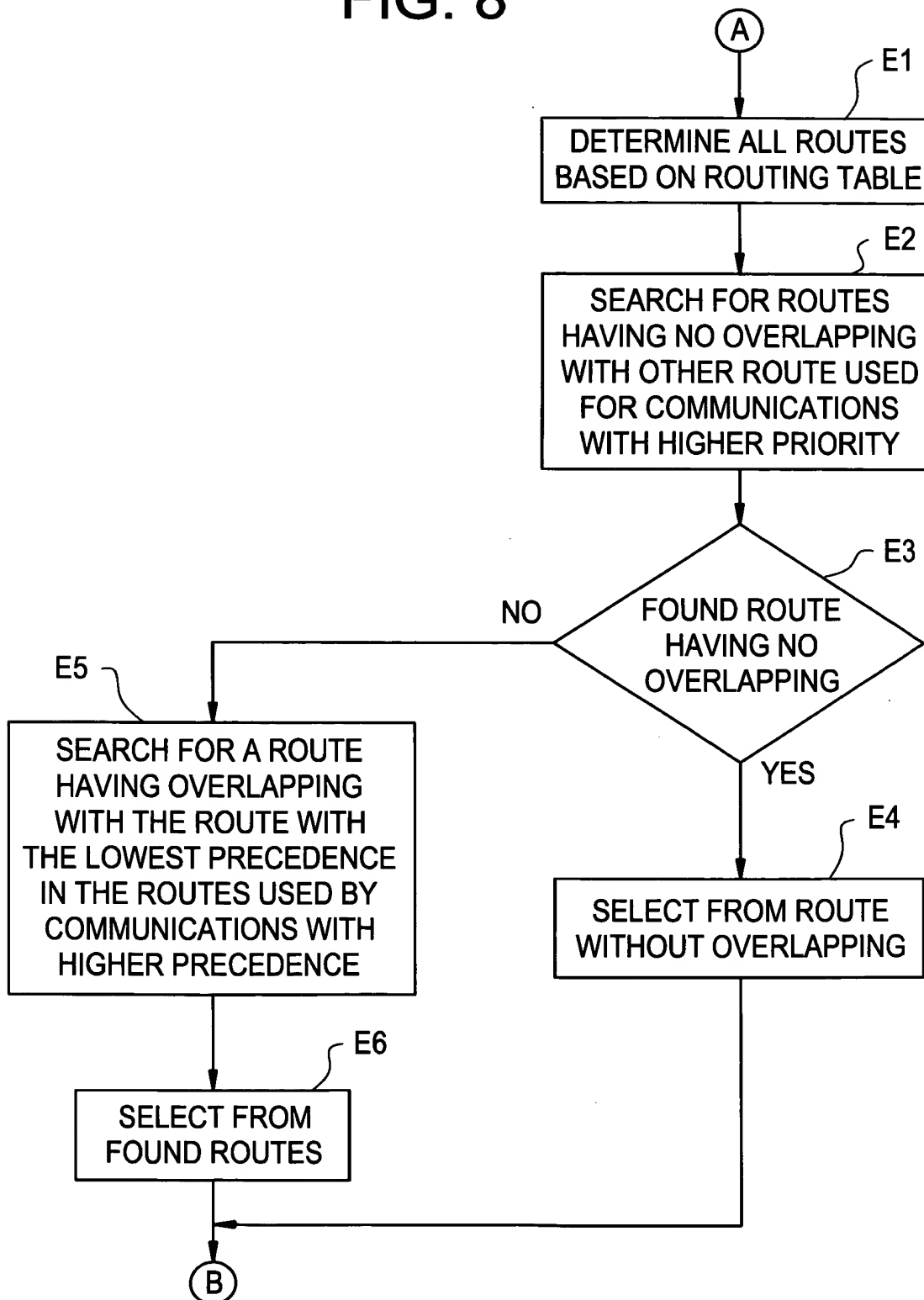






FIG. 9

